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February 12, 2004

ATTORNEY DOCKET NO. 03180.0353
CUSTOMER NO. 22,852



Mail Stop Patent Application

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

New U.S. Patent Application
Title: INSPECTION METHOD, PROCESSOR AND METHOD FOR
MANUFACTURING A SEMICONDUCTOR DEVICE
Inventor's Name and Address:

Kazuya FUKUHARA
Tokyo, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 40 pages, including 3 independent claims and 20 claims total.
2. Drawings- 18 sheets of drawings containing 22 Figures (Figures 1-22).
3. Certified copy of Japanese Patent Application No. 2003-035383, filed February 13, 2003.
4. Information Disclosure Statement and Information Disclosure Citation, and PTO 1449.
5. The filing fee is calculated as follows:



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Basic Application Filing Fee					\$770	\$ \$770.00
	Number of Claims		Basic	Extra Claims		
Total Claims	20	-	20	0	x \$18	
Independent Claims	3	-	3	0	x \$86	
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$290	\$
Subtotal						\$ 770.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 770.00

6. A check for \$770.00 is enclosed. The fee includes:

\$770.00 filing fee

This application is being filed under the provisions of 37 C.F.R. § 1.53(f).
Applicant awaits notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicant claims the right to priority based on Japanese Patent Application No. 2003-035383, filed February 13, 2003.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date.

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: _____



Ernest F. Chapman
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EFC/FPD/cma
Enclosures